WEST Search History

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DATE: Wednesday, April 11, 2007

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	DB=EP	$PAB, JPAB, DWPI, TDBD; \ PLUR=NO$; OP=ADJ	
	L56	port\$1 with L55	. 43	
Γ	L55	bank\$1 with register\$1	1998	
	DB=PC	GPB, USPT; PLUR=NO; OP=ADJ	•	
П	L54	tetsuya.in. and 153	0	
	L53	morita.in. and nec.as.	66	
Γ:	L52	morita.in. and nec.as. and size.ti.	0	
	DB=EP	PAB,JPAB,DWPI,TDBD; PLUR=NO	; OP=ADJ	
	L51	146 and L50	. 1	
Γ.	L50	144 or 145	2	
	L49	64-bit port\$1	. 0	
Γ.	L48	64 bit port\$1	0	
Γ	L47	32-bit port\$1	. 3	
	L46	32 bit port\$1	. 3	
	L45	16 bit port\$1	2	
. Г	L44	16-bit port\$1	1	
.	· L43	16-bit port	. 1	
Γ.	L42	variable bit width	10	
DB=PGPB, USPT; PLUR=NO; OP=ADJ				
Γ.	L41	variable bit width	. 84	
Fi.	L40	L24 and L25	3	
Γ	L39	L23 and L25	1	
	L38	L30 and L36	4.	
Γ	L37	L14 and L36	1	
Γ	L36	L23 and L24	37	
Γ	L35	L27 and L25	1	
Γ:	L34	L30 and L32	3	
Г	L33	L14 and L32	0	
Γ	L32	L27 and L24	15	
Γ	L31	L28 and L30	15	
Γ.	L30	bank\$1 with register\$1	9510	
Γ	L29	L14 and L28	. 1	

Γ	L28	L23 and L27	44
Γ.	L27	8-bit port\$1	354
Γ	L26	128-bit port\$1	3
Γ	L25	64-bit port\$1	37
Γ	L24	32-bit port\$1	82
Γ	L23	16-bit port\$1	306
Γ	L22	theis.in. and data path architectures	0
· []	L21	port\$1 and L20	0
<u></u>	L20	5754875.pn.	1.
Γ	L19	port\$1 and L18	42
Γ:	L18	data type\$1 and L17	69
Γ	L17	(712/225).ccls.	629
	L16	data type\$1 and L14	. 22
Π	L15	data type\$1 with L14	0
	L14	(711/149).ccls.	393
	L13	data type\$1 with L3	0
	L12	L3 and L11	0
	L11	input port\$1 with data type\$1	61
П	L10	L7 same L8	3
	L9	L7 same I5L5	0
	L8	input port\$1 with floating point	42
	L7	input port\$1 with integer	546
Π.	L6	L4 same L5	162
Γ.;	L5	port\$1 with floating point	473
	, L4	port\$1 with integer	2358
	L3	6343356.uref.	5
	L2	port\$1 and L1	1
Π	L1	6343356.pn.	1

END OF SEARCH HISTORY